

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY-HYDERABAD-500085

EXAMINATION BRANCH

M.TECH-I SEMESTER -R17 REGULATION-REGULAR/ SUPPLEMENTARY EXAMINATIONS DECEMBER 2018

T I M E T A B L E

MB
TR
27/11/18

TIME: 10:00 AM TO 1:00 PM

Course	17-12-2018 MONDAY	19-12-2018 WEDNESDAY	21-12-2018 FRIDAY	24-12-2018 MONDAY	28-12-2018 FRIDAY	31-12-2018 MONDAY	02-01-2019 WEDNESDAY	04-01-2019 FRIDAY
ELECTRONICS AND COMMUNICATI ON ENGINEERING .70	Digital System Design	ELECTIVE-I	ELECTIVE-I	ELECTIVE-II	Advanced Digital Signal Processing	OE-I	Coding Theory and Techniques	--
		Detection and Estimation Theory	Digital Signal Processors and Architectures	Real Time Operating Systems		Computer Oriented Numerical Methods		
			Speech Signal Processing	Adhoc Wireless Sensor Networks		Renewable Energy Systems		
				Advanced Computer Architecture		Electrical Installation And Safety		
						Optimization Techniques And Applications		
	Fundamentals Of Cyber Security							

DATE : 24-10-2018

Sd/-
CONTROLLER OF EXAMINATION

NOTE:

(I). ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY (II). EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES.THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

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EXAMINATION BRANCH

M.TECH-I SEMESTER -R15 REGULATION- SUPPLEMENTARY EXAMINATIONS DECEMBER-2018

TIME TABLE

TIME : 10:00 AM TO 1:00 PM

NB
AR
21/11/18

COURSE	17-12-2018 MONDAY	19-12-2018 WEDNESDAY	21-12-2018 FRIDAY	24-12-2018 MONDAY	28-12-2018 FRIDAY	31-12-2018 MONDAY	02-01-2019 WEDNESDAY	04-01-2019 FRIDAY
ELECTRONICS AND COMMUNICATION ENGINEERING~70	DIGITAL SYSTEM DESIGN	VLSI TECHNOLOGY AND DESIGN	ADVANCED DATA COMMUNICATIONS	ELECTIVE-C2	ELECTIVE-C1	OPEN ELECTIVE VIRTUAL INSTRUMENTATION	ELECTIVE-C1 DETECTION AND ESTIMATION THEORY	ELECTIVE-C2 ADVANCED COMPUTER ARCHITECTURE
				INTERNETWORKING	EMBEDDED SYSTEM	ADVANCED COMMUNICATIONS AND NETWORKS SPREAD SPECTRUM COMMUNICATION		
				REAL TIME OPERATING SYSTEMS	DESIGN			
					ADVANCED DIGITAL SIGNAL PROCESSING			
HIGHWAY ENGINEERING~87	URBAN TRANSPORTATION PLANNING	TRAFFIC ENGINEERING	PAVEMENT MATERIAL CHARACTERIZATION	ELECTIVE-C1	ELECTIVE-C2	OPEN ELECTIVE REMOTE SENSING AND GIS	ELECTIVE-C1 ROAD SAFETY ENGINEERING	ELECTIVE-C1 BRIDGE ENGINEERING
				ENGINEERING OF GROUND	HIGHWAY INFRASTRUCTURE DESIGN	PROJECT MANAGEMENT		
					ADVANCED CONCRETE TECHNOLOGY	APPLIED STATISTICS		
					TRANSPORTATION SYSTEM MANAGEMENT			

DATE:24-10-2018

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EXAMINATION BRANCH

M.TECH-II SEMESTER -R15 REGULATION- SUPPLEMENTARY EXAMINATIONS DECEMBER-2018

T I M E T A B L E

TIME : 10:00 AM TO 1:00 PM

MS
27/11/18

COURSE	18-12-2018 TUESDAY	20-12-2018 THURSDAY	22-12-2018 SATURDAY	27-12-2018 THURSDAY	29-12-2018 SATURDAY	01-01-2019 TUESDAY	03-01-2019 THURSDAY	05-01-2019 SATURDAY
Electronics and Communication Engineering-70	--	Digital Signal Processors and Architectures	(Elective –IV) Wireless MIMO Communications Optical Communications Technology	(Elective –III) Speech Signal Processing	(OE2) Image and Video Processing	Wireless Communications and Networks	(Elective –IV) Network Security And Cryptography	Coding Theory and Techniques
				Biomedical Signal Processing	Software Defined Radio			
				Radar Signal Processing	Adhoc Wireless Networks			
Highway Engineering – 87	Traffic Analysis	(Elective –III) Land use and Transportation Modeling	(Elective –III) Environmental Impact Assessment for Transportation Projects	Pavement Analysis and Design	(OE2) Optimization Techniques	(Elective –IV) Intelligent transportation systems	Pavement Construction, Maintenance & Management	(Elective –III) Highway Project Formulation and Economics
					Disaster Management			
					Finite Element Method			
						Rural Roads		
	Airport Engineering							

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